

A one time programmable solid-state device is provided such that the programmable solid-state device includes a programmable memory unit embedded in a die and a driver circuit that programs the programmable memory unit. The one time programmable solid-state device also includes an access circuit that enables access to the programmable memory unit. In an example operation when the one-time programmable solid-state is a solid-state image device, the one-time programmable solid-state device performs the process of identifying an address of a defective pixel in a photosensor having a plurality of pixels arranged in a two-dimensional array in the die and storing the address in the programmable memory unit that is embedded in the die of the solid-state imaging device.